Hardware

Advant Controller 31 Intelligent Decentralized Automation System

Basic Units 707 KT 97, 07 KT 96, 07 KT 95

S ^o ARR

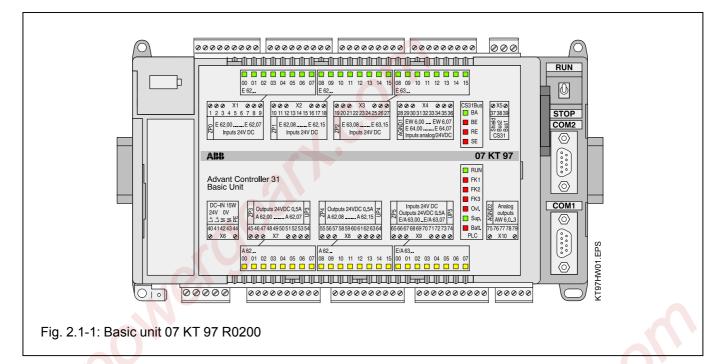


2.1 Basic Unit 07 KT 97

Basic unit with max. 480 kB user program

+ 256 kB user data, CS31 system bus

The basic unit 07 KT 97 R200 is the standard device for all applications. In addition, there are basic units with reduced performance (e.g. 07 KT 95 or 07 KT 96) as well as ones with extended performance (e.g. 07 KT 97 R260 with ARCNET connection, 07 KT 97 R0220 with PROFIBUS connection and 07 KT 97 R0262 with both ARCNET and PROFIBUS connection). A comparison table is given on page 3. This document describes the basic unit 07 KT 97 R200 and then adds the data sheets of the other devices which only show the differences.



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Functionality of the basic units 07 KT 97

-	
User program User data	480 kB 256 kB (Flash EPROM)
Digital inputs Digital outputs Digital inputs/outputs	24 in 3 groups of 8 each, electrically isolated 16 transistor outputs in 2 groups of 8 each, electrically isolated 8 in 1 group, electrically isolated
Analog inputs	8 in 1 group, individually configurable to 010 V, 05 V, +10 <mark>V</mark> , +5 V, 020 mA, 420 mA, Pt100 (2-wire or 3-wire), differential inputs, digital inputs
Analog outputs	4 in 1 group, individually configurable to 010 V, 020 mA, 420 mA
Serial interfaces	COM1, COM 2 as MODBUS interfaces and for programming and test functions
Parallel interfaces for connection of couplers	07 KP 90 (RCOM), 07 KP 93 (2 x MODBUS), 07 MK 92 (freely programmable)
System bus interface	CS31
Integrated couplers	see next page
High-speed counter	integrated, many functions configurable
Real-time clock	integrated
SmartMedia Card	memory medium for operating system, user program and user data
LED displays	for signal conditions, operating statuses and error messages
Power supply voltage	24 V DC
Data backup	with lithium battery 07 LE 90
Programming software	907 AC 1131

Differences between the basic units 07 KT 95 to 07 KT 98

Basic unit	07 KT 95	07 KT 96	07 KT 97	07 KT 98		
Number of digital inputs Number of digital outputs No. of digital inputs/outputs	12 8 -	24 16 -	24 16 8	24 16 8	102	
Number of analog inputs Pt100 Number of analog outputs 20 mA	4 no 2 no	- - -	8 yes 4 yes	8 yes 4 yes	0.	
Are the analog inputs con- figurable as digital inputs?	no	-	yes	yes		
Terminals 20 to 27	- -	E 63,00 to E 63,07	E 63,08 to E 63,15	E 63,08 to E 63,15		
Processing time, 65 % bits, 35 % words, for 1 kB of program, typ.	0.3 ms	0.3 ms	0.3 ms	0.07 ms		
Order number	GJR5 2528 00 R	GJR5 2529 00 R	GJR5 2530 00 R	GJR5 2531 00 R		

Available versions of the basic units 07 KT 95 to 07 KT 98

Version of the basic unit	Integrated (internal) couplers	Version is available with 07 KT 95 07 KT 96 07 KT 97 07 KT 98			
R0100, R0200	none	•	•	•	
R0120, R0220	PROFIBUS-DP			•	C ·
R0160, R0260	ARCNET			•	
R0162, R0262	ARCNET + PROFIBUS-DP			•	•
R0268	ARCNET + CANopen				•
R0270	Ethernet			•	•
R0272	Ethernet + PROFIBUS-DP		3	•	•
R0276	Ethernet + ARCNET			•	•
R0277	Ethernet + Ethernet			•	•
R0278	Ethernet + CANopen			•	•
R0280	CANopen			•	•

Usable SmartMedia Cards

Version of the	Usable SmartMedia Card				
basic unit	07 MC 90, 5 V GJR5 2526 00 R0 1 01 07 MC 90, 3.3 V GJR5 2526 00 R0 2 01				
R0100 to R0199 R0 2 00 to R0 2 99	50	•			

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2.1.1 Brief description

The basic unit 07 KT 97 works either as

- bus master in the decentralized automation system Advant Controller 31 or as
- slave (remote processor) in the decentralized automation system Advant Controller 31 or as
- stand-alone basic unit.

The basic unit is powered by 24 V DC.

2.1.1.1 Main features

- 24 digital inputs with LED displays
- 16 digital transistor outputs with LED displays
- 8 digital inputs/outputs with LED displays
- 8 individually configurable analog inputs 0...10 V, 0...5 V, ±10 V, ±5 V, 0...20 mA, 4...20 mA, differential inputs, Pt100 (2-wire or 3-wire), the analog inputs are also individually configurable as digital inputs
- 4 individually configurable analog outputs ±10 V, 0...20 mA, 4...20 mA
- 2 counters for counting frequencies up to 50 kHz, configurable in 7 different operating modes
- 1 CS31 system bus interface for system expansion
- 1 interface for connecting communication modules (e.g. 07 KP 90)
- 2 serial interfaces COM1, COM2

 as MODBUS interfaces and
 - for programming and test functions
- Real-time clock
- LEDs for displaying operating conditions and error messages
- Detachable screw-type terminal blocks
- Fastening by screws or by snapping the device onto a DIN rail
- The lithium battery 07 LE 90 can be put into the battery compartment in order to
 - store and backup the user program in the RAM
 - store and backup data which is additionally contained in the RAM, e.g. the status of flags
 - backup the time and date (real-time clock)
- RUN/STOP switch for starting and aborting the program execution
- Extensive diagnosis functions
 - self-diagnosis of the basic unit
 - diagnosis of the CS31 system bus and the connected modules

- Integrated Flash EPROM for storing program and data
- Exchangeable SmartMedia Card 07 MC 90 for user data or for updating the operating system or PLC program

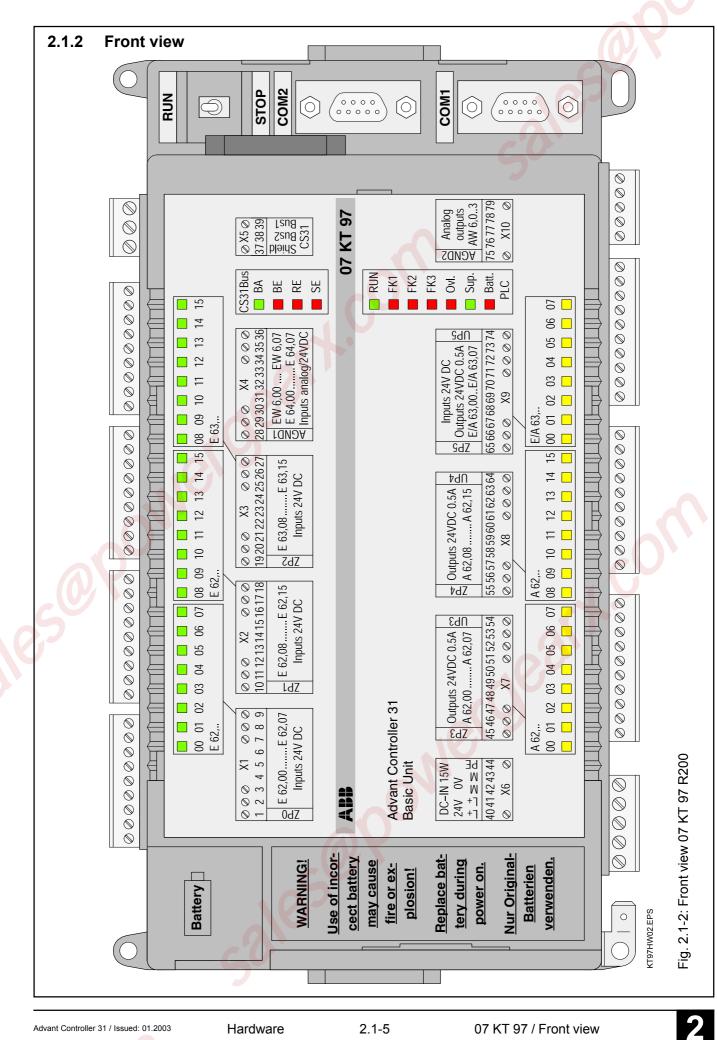
2.1.1.2 Project planning / start-up

The following has to be observed for project planning and start-up:

- Programming is performed with AC31 programming software, which can be run on commercially available IBM compatible PCs (see documentation of the programming system 907 AC 1131).
- Online program modification A quick modification of the user program is possible without interrupting the operation (see programming system 907 AC 1131).
- Possible operating modes
 - Stand-alone basic unit
 - Bus master basic unit
 - Slave basic unit
- Backup of data areas,
 i.e. saving of data during power OFF/ON, is possible with an integrated battery and/or

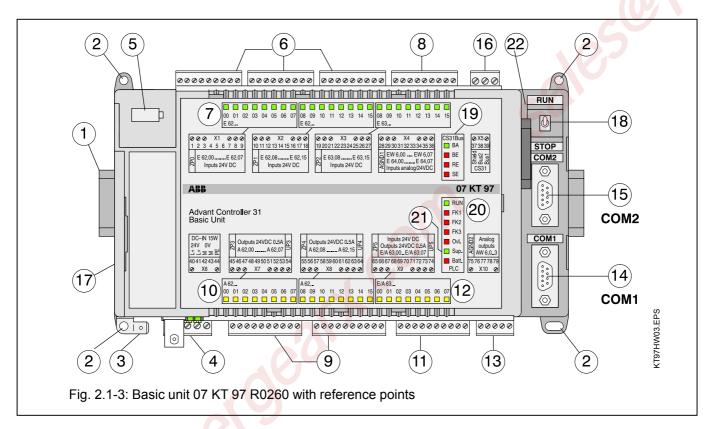
by storing them in the Flash EPROM.





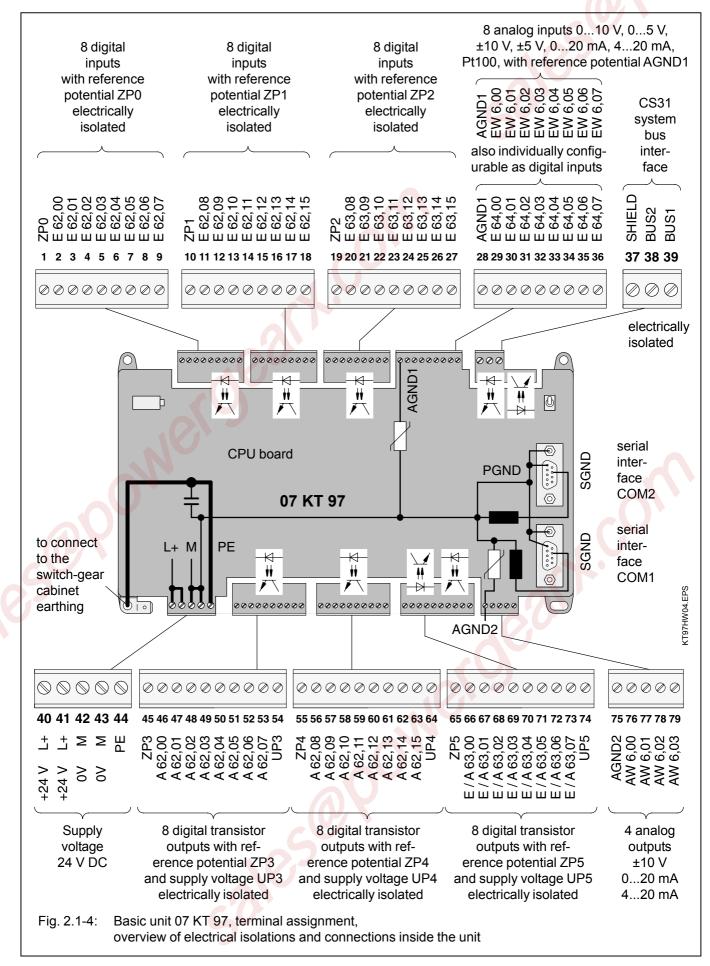
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2.1.3 Structure of the front panel



- (1) Fastening the device on DIN rail
- (2) Fastening the device by screws
- (3) Faston earthing terminal 6.3 mm
- (4) Supply voltage connection 24 V DC
- (5) Battery compartment
- (6) 24 digital inputs in 3 groups
- (7) 24 green LEDs for the digital inputs
- 8 individually configurable analog inputs in one group 0...10 V, 0...5 V, ±10 V, ±5 V, 0...20 mA, 4...20 mA, Pt100 (2-wire or 3-wire), differential inputs, the analog inputs are also individually configurable as digital inputs
- (9) 16 digital transistor outputs in two groups
- (10) 16 yellow LEDs for the digital outputs
- (11) 8 digital inputs/outputs in one group
- (12) 8 yellow LEDs for the digital inputs/outputs
- (13) 4 individually configurable analog outputs ±10 V, 0...20 mA, 4...20 mA in one group
- (14) Serial interface COM1 (programming, MMC)
- (15) Serial interface COM2 (programming, MMC)

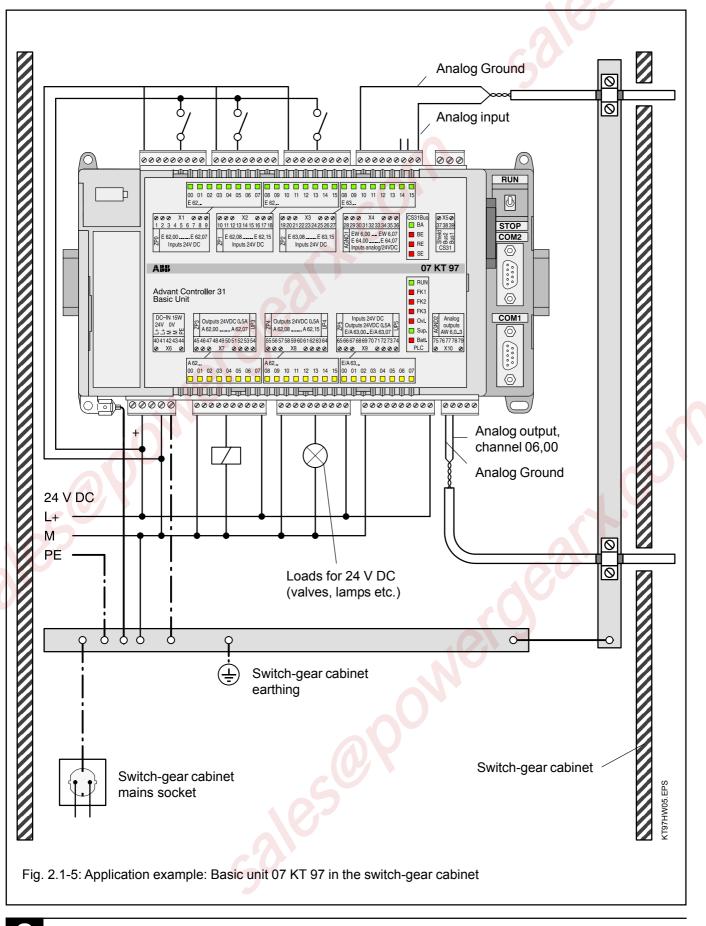
- (16) Connection for CS31 system bus
- (17) Cover of the interface for the connection of communication modules (may only be removed for connecting communication modules)
- (18) Switch for RUN/STOP operation: With the RUN/STOP switch the execution of the user program is started or stopped.
- (19) LED displays for CS31 system bus BA LED green Bus active BE LED red Bus error RE LED red Remote unit error SE LED red Serial unit error
- (20) LED displays for RUN and error class
 RUN LED green User progr. is running
 FK1 LED red Fatal error
 FK2 LED red Serious error
 FK3 LED red Light error
- (21) Other LED displays Over- LED red Overload/short-circuit load at an output Supply LED green Supply voltage available Battery LED red Batt. **not** effective
- (22) Insertable SmartMedia Card 07 MC 90 for operating system, user program and user data



2.1.4 Electrical connection

2.1.4.1 Application example for input and output wiring

The following illustration shows an application example in which different possibilities for wiring inputs and outputs are used.



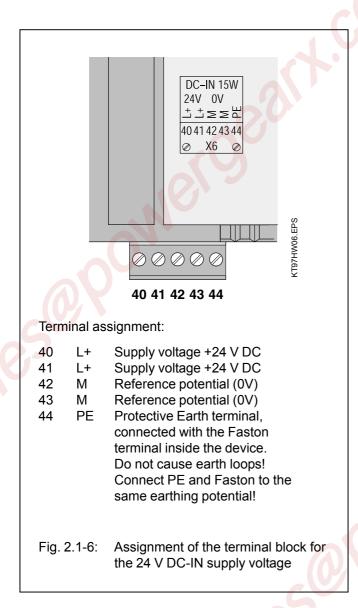
Please observe in particular:

- The earthing measures
- The handling of the electrically isolated input groups
- The handling of the electrically isolated output groups
- The connection of shielded analog cables
- The earthing of the switch-gear cabinet mains socket

2.1.4.2 Connection of the supply voltage

The 24 V DC supply voltage is connected via a 5-pole detachable screw-type terminal block.

Attention: Plug and unplug terminal block only with power is off!

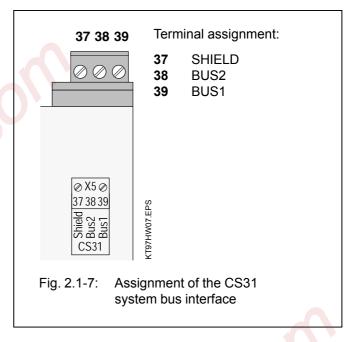


The terminals 40 and 41 (L+) as well as 42 and 43 (M) are connected to each other via the printed circuit board. If the power supply is looped through, these two connections must not be burdened with currents higher than 4 A.

Please take also into consideration that supply voltages which are looped through are disconnected for the following devices when the plug is withdrawn.

If higher currents are to be conducted without interruption possibility, the two wires for M have to be connected under the same terminal. The same applies for L+.

2.1.4.3 Connection for the CS31 system bus



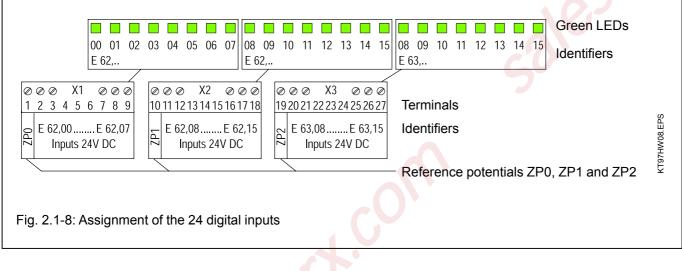
The connection to the CS31 system bus is made by means of a 3-pole detachable terminal block. Please observe:

- All of the AC31 devices, no matter whether they are master or slave devices, are connected with twistedpair bus line as follows:
 - One core of the bus line is looped through via the BUS1 terminals of all devices to be connected to the CS31 system bus.
 - The other core of the bus line is looped through via the BUS2 terminals of all devices to be connected to the CS31 system bus.
- If the basic unit 07 KT 97 is located at the beginning or at the end of the bus line, the bus terminating resistor (120 Ω) has to be connected additionally between the BUS1 and BUS2 terminals.
- The shield of the twisted-pair bus line is looped through via the SHIELD terminals of all the devices to be connected to the CS31 system bus.
- The handling of the CS31 system bus is described in detail in volume 2, System data.



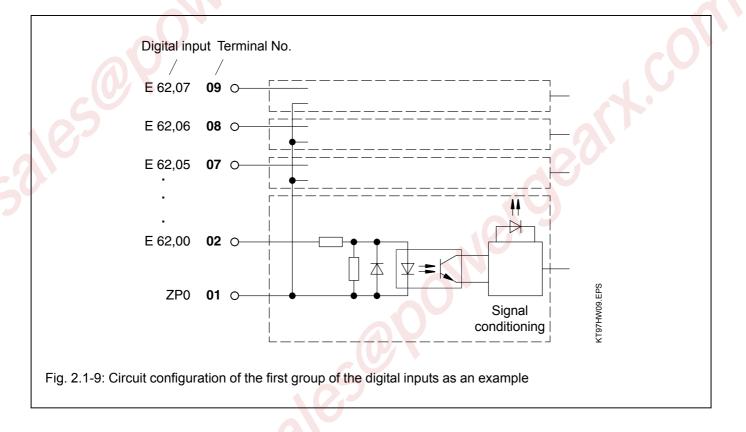
2.1.4.4 Connection of the digital inputs

The following figure shows the assignment of the 24 digital inputs.



Features:

- The 24 digital inputs are arranged in three groups of 8 inputs each.
- The three groups E 62,00...E 62,07, E 62,08...E 62,15 and E 63,08...E 63,15 are electrically isolated from each other.
- The inputs use 24V signals in positive logic (1 = +24 V).
- The signal delay of the inputs is configurable to 7 ms (default) or 1 ms (see "System technology").
- The circuit configuration of the first group of the digital inputs is shown as an example in the following.



2.1.4.5 Connection of the digital outputs

The following figure shows the assignment of the 16 digital outputs.

	Supply voltages UP3 and UP4 Reference potentials ZP3 and ZP4
∽ Outputs 24VDC 0.5A ∽ ▼ Outputs 24VDC 0.5A ▼	
C A 62,00 A 62,07 C A 62,07 C A 62,08 A 62,15	Identifiers
45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64	Terminals
<u> </u>	
	PS
A 62, A 62,	Identifiers
00 01 02 03 04 05 06 07 08 09 10 11 12 13 14 15 🧹 🔨	1 AF
	Identifiers Yellow LEDs
	Y
1-10: Assignment of the 16 digital outputs	

Features of the digital outputs:

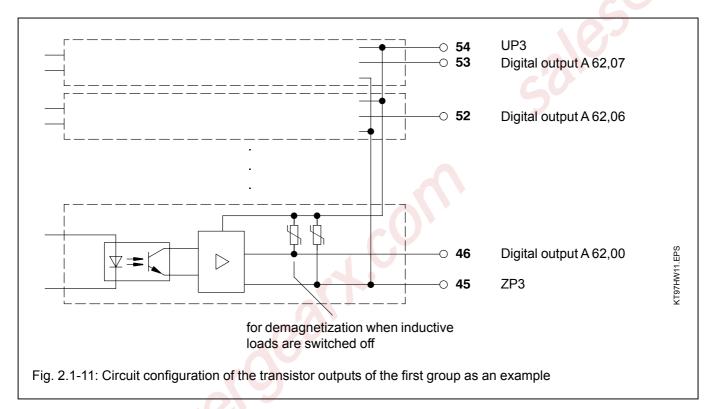
- The 16 digital outputs are arranged in two groups of 8 outputs each.
- The two groups are electrically isolated from each other.
- The outputs can be loaded with a rated current of 500 mA.
- Each group as a whole is electrically isolated from the rest of the device.
- The outputs employ semiconductors and are shortcircuit and overload-proof.

- The outputs are automatically switched off in case of overload or short-circuit.
- An overall error message indicates whether a shortcircuit or an overload has occurred on a output group.
- The overload is displayed by the red LED Ovl. and via error flags in the PLC.
- The red LED Ovl. goes out when the overloaded output is switched on again automatically.
- The outputs are safe against reverse polarity and forced supply of 24 V DC.

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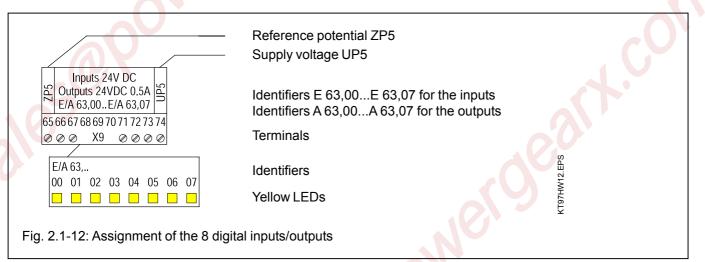
Circuit configuration of the digital outputs

The following figure shows the circuit configuration of the digital outputs of the first group as an example.



2.1.4.6 Connection of the digital inputs/outputs

The following figure shows the assignment of the 8 digital inputs/outputs.



Features of the digital inputs/outputs:

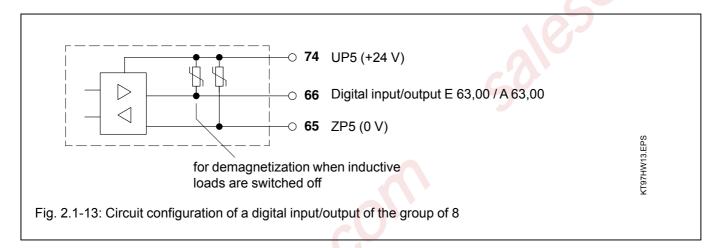
- The 8 digital inputs/outputs are arranged in one group.
- The group as a whole is electrically isolated from the rest of the device.
- The inputs/outputs can be used individually as input, output or re-readable output.
- If the terminals are used as digital inputs, the input signal delay can be configured to 7 ms (default) or to 1 ms (see "System technology").

If the terminals are used as digital outputs, the output signals "1" are individually monitored by the re-readable input. If the output status is wrong, an overall error message is generated for the involved output group. The error is displayed by the red LED Ovl. and by error flags of the PLC then. The error could have been caused by overload, short-circuit or missing supply voltage UP5/ZP5. The technical specifications of the outputs are the same as with the other digital outputs.



Circuit configuration of the digital inputs/outputs

The following figure shows one of the 8 inputs/outputs of the group as an example.



 The technical specifications of the inputs are the same with the other digital inputs, but with the following exception:

Caused by the direct electrical connection with the output, the varistor for demagnetization of inductive loads (see figure above) is also in effect at the input.

Therefore, the voltage difference between UP5 and the input signal must not be greater than the limit voltage of the varistor.

The limit voltage of the varistor is ca. 36 V. This means, that if UP5 = 24 V, the input signal voltage must be between -12 V and +30 V. If UP5 = 30 V, the input voltage has to be within -6 V and +30 V.

2.1.4.7 Connection of the 8 configurable analog inputs

The following figure shows the assignment of the 8 analog inputs.

If all of the 8 channels of the group are used as inputs, and if in addition the UP5 terminal is left unconnected, no restrictions exist for the inputs. The input signal voltages then may be within -30 V and +30 V.

There is no restriction for the input/output group concerning its safety against reversed polarity.

/		– Re
Ø	Ø Ø X4 Ø Ø Ø 29 30 31 32 33 34 35 36	Те
GND1	EW 6,00 EW 6,07 E 64,00 E 64,07 Inputs analog/24VDC	lde Ide

Reference potential AGND1

erminals

Identifiers EW 6,00...EW 6,07 if used as analog inputs Identifiers E 64,00...E 64,07 if used as digital inputs

Fig. 2.1-14: Assignment of the 8 analog inputs

Features of the analog inputs:

- The 8 analog inputs are **not** electrically isolated.
- Resolution in the PLC system: The measured values are converted with a resolution of 12 bits, i.e. 11 bits plus sign for voltage and 12 bits without sign for currents. The ranges 0...5 V and ±5 V are converted with 10 bits plus sign.
- Analog signals are conducted in shielded cables (see Fig. 2.1-5).
- The analog inputs can be used individually in a lot of different operating modes (even as digital inputs).
 The operationg modes are configurable.
- In order to make sure, that unused input channels have a defined 0V level, they may be shorted to AGND.

In the following, some application examples are shown for analog sensors.

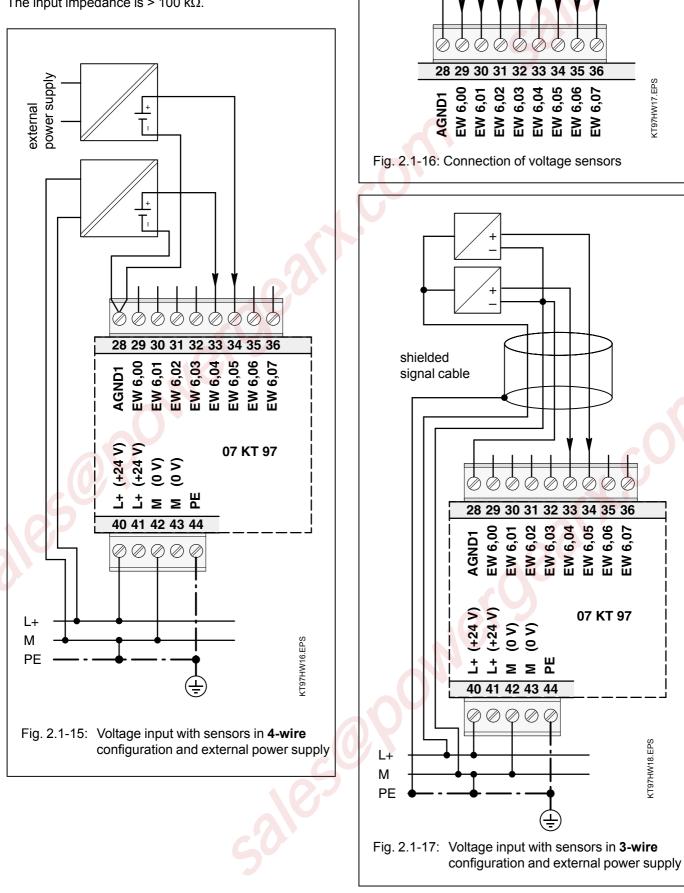


KT97HW14.EPS

Measuring ranges ±10 V / ±5 V / 0...10 V / 0...5 V

Input voltages which exceed the measuring range cause an overflow error message. If the measured value is below the range, an underflow error message is generated.

The input impedance is > 100 k Ω .



KT97HW18.EPS

KT97HW17.EPS

Measuring range 4...20 mA (passive-type 2-pole sensors)

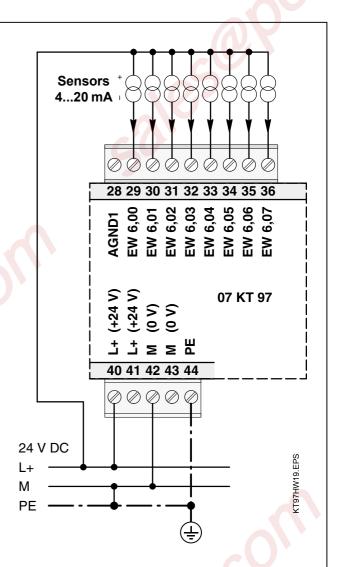
Input currents which exceed the measuring range cause an overflow error message. If the measured value is below the range, an underflow error message is generated.

The input impedance is ca. 330 Ω . The current input has a self-protecting mechanism. If the input current gets too high, the shunt is switched off and the value for range overflow is generated. About every second, the unit tries to switch on the shunt again. In this way the correct measurement will succeed after the current has reached a normal value again.

The trigger of the self-protecting mechanism is displayed by the red LED OvI. as long as the overload is present. In the PLC system an error message is then stored (FK4, error number 4).

The open-circuit monitoring begins below ca. 3 mA. The value of the range underflow is stored. If the open-circuit monitoring is configured, the open-circuit event is displayed by the red LED OvI. as long as it is present. In the PLC system an error message is stored (FK4, error number 9).

The following figure shows the connection of **2-pole pas**sive-type analog sensors 4...20 mA.



If the analog current sensors 4...20 mA are powered from a separate power supply unit, the reference potentials 0V (of the separate power supply unit and the power supply unit for the 07 KT 97) must be interconnected to each other.

In the above example, the AGND terminal remains unused.

Fig. 2.1-18: Example for the connection of current sensors 4...20 mA at the analog inputs

Measuring range 0...20 mA (active-type sensors with external supply voltage)

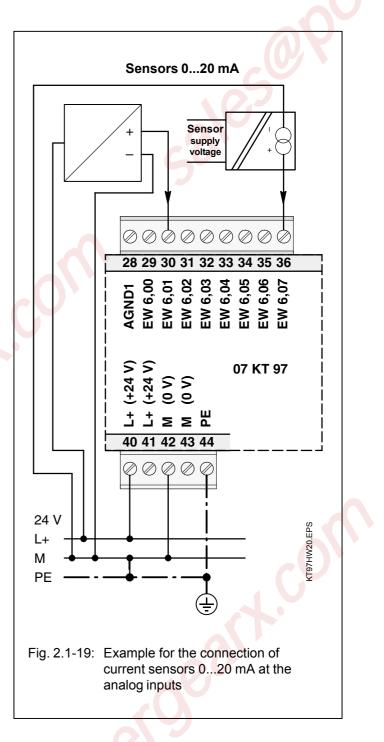
Input currents which exceed the measuring range cause an overflow error message. If the measured value is below the range, an underflow error message is generated.

The input impedance is ca. 330 Ω . The current input has a self-protecting mechanism. If the input current gets too high, the shunt is switched off and the value for range overflow is generated. About every second, the unit tries to switch on the shunt again. In this way the correct measurement will succeed after the current has reached a normal value again.

The trigger of the self-protecting mechanism is displayed by the red LED OvI as long as the overload is present. In the PLC system an error message is then stored (FK4, error number 4).

The following figure shows the connection of a 3-wire sensor powered by 24 V DC **and** of a 2-pole sensor powered electrically isolated. Both sensors work as **active current sources** 0...20 mA.

It has to be taken into consideration, that in this application the M terminal of the basic unit is the reference potential. AGND1 is not dimensioned for carrying the sum of the sensor currents.



Measuring ranges ± 10 V / ± 5 V / 0...10 V / 0...5 V as differential inputs

Differential inputs are very useful, if analog sensors are used which are remotely non-isolated (e.g. the minus terminal is remotely earthed).

Since the earthing potential is not exactly the same as AGND1, it has to be measured bipolar in order to compensate measuring errors. Additionally, in case of single-pole configuration, AGND1 would be connected directly to the remote earth potential. This would cause inadmissable (and possibly dangerous) earthing loops.

In all configurations using **differential inputs** two adjacent analog inputs belong together (e.g. EW 6,00 and EW 6,01).

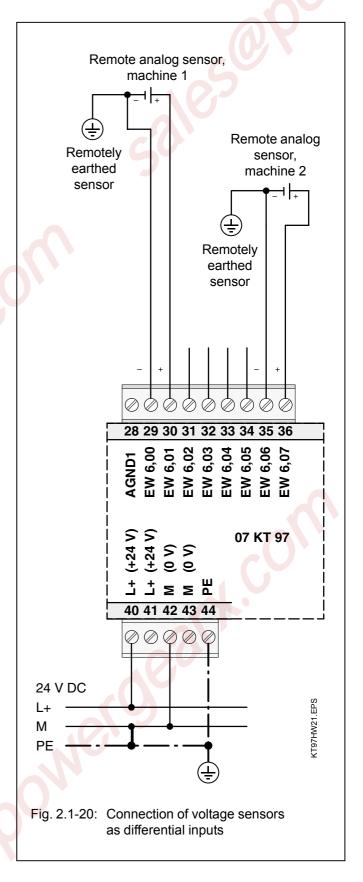
The measured value is calculated by subtraction. The value of the channel with the lower address is subtracted from the value of the channel with the higher address.

The converted measured value is available on the odd address (e.g. EW 6,01).

Important:

The common mode input voltage range equals the measuring range of the single channel. I.e. that the signals, related to AGND, at the two involved inputs must not exceed this measuring range.

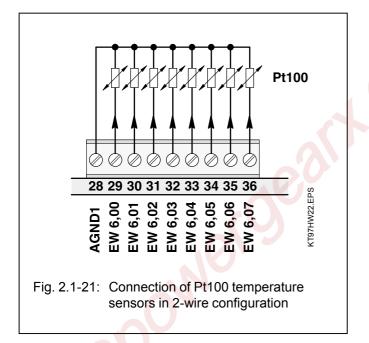
Input voltages which exceed the measuring range cause an overflow error message. If the measured value is below the range, an underflow error message is generated.



Measuring ranges -50°C...+400°C and -50°C...+70°C with Pt100 as temperature sensor in 2-wire configuration

When resistance thermometers are used, a constant current must flow through the measuring resistor in order to create the necessary voltage drop for the evaluation. For this purpose, the basic unit 07 KT 97 provides a constant current sink, which is multiplexed to the 8 analog channels.

The following figure shows the connection of Pt100 resistance thermometers in **2-wire configuration**.



Depending on the configured operating mode, the measured value is assigned linearly as follows:

Range assigned numerical value range

-50 C400°C	-1022+8190	(FC02 _µ 1FFE _µ)
-50 C70°C		(FC02 _H 0599 _H)

The basic unit linearizes the Pt100 characteristic.

Temperatures which exceed the measuring range cause an overflow error message. If the measured value is below the range, an underflow error message is generated.

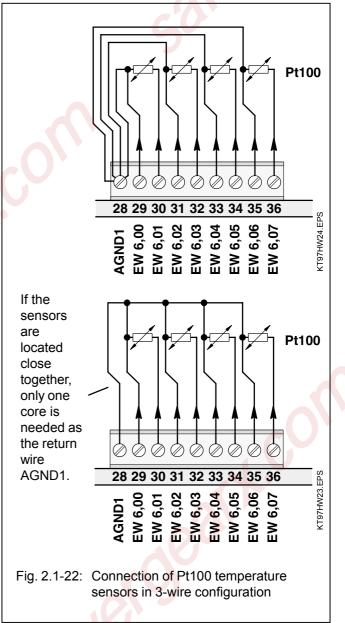
A detected open-circuit causes an overflow error message. If the sensor is short-circuited, an underflow error message is generated.

If the open-circuit or short-circuit monitoring is configured, the detected error is displayed by the red LED Ovl as long as it is present. In the PLC system an error message is stored (FK4, error number 9).

In order to avoid error messages with unused analog inputs, it is useful, **not** to configure this channels for Pt100.

Measuring ranges -50°C...+400°C and -50°C...+70°C with Pt100 as temperature sensor in 3-wire configuration

The following figure shows the connection of Pt100 resistance thermometers in **3-wire configuration**.



In the operating mode **"Pt100 in 3-wire configuration"** two adjacent analog inputs belong together (e.g. EW 6,00 and EW 6,01).

For configuration, both inputs must be configured to the desired operating mode.

The constant current of the one channel flows through the Pt100 resistance sensor, the constant current of the other channel through one of the wires.

The basic unit calculates the measuring value from the two voltage drops and stores it under the odd address (e.g. EW 6,01).



In order to avoid measurement errors, it is absolutely necessary, to lead the cores to the Pt100 sensors in the same cable. The cores must have the same cross section. Per channel, a twisted pair is used (for the two terminals of the Pt100 sensors) plus a single core (half of a twisted pair) for the connection to AGND1.

Depending on the configured operating mode, the measured value is assigned linearly as follows:

Range assigned numerical value range

-50 C400°C	-1022+8190	(FC02 _H 1FFE _H)
-50 C70°C		(FC02 _H 0599 _H)

The basic unit linearizes the Pt100 characteristic.

Temperatures which exceed the measuring range cause an overflow error message. If the measured value is below the range, an underflow error message is generated.

A detected open-circuit causes an overflow error message. If the sensor is short-circuited, an underflow error message is generated.

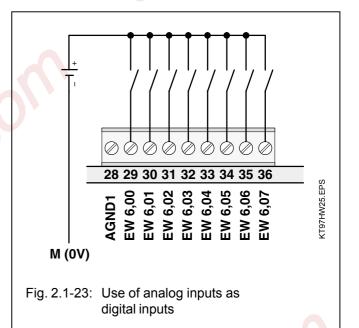
If the open-circuit or short-circuit monitoring is configured, the detected error is displayed by the red LED Ovl as long as it is present. In the PLC system an error message is stored (FK4, error number 9).

In order to avoid error messages with unused analog inputs, it is useful, **not** to configure this channels for Pt100.

Use of analog inputs as digital inputs

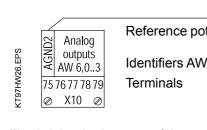
Several (or all) analog inputs can be configured as digital inputs. When doing so, they evaluate input voltages higher than ca. +7 V as signal 1. The input impedance in this operating mode is about 4 k Ω . Terminal M is the reference potential.

The input signal delay is 7 ms. It cannot be configured. The inputs are not electrically isolated.



2.1.4.8 Connection of the 4 configurable analog outputs

The following figure shows the assignment of the 4 configurable analog outputs.



Reference potential AGND2 Identifiers AW 6,00...AW 6,03

Fig. 2.1-24: Assignment of the 4 analog outputs

Features of the analog outputs:

- The 4 analog outputs are **not** electrically isolated...
- Resolution in the control system: All analog output values are converted with a resolution of 12 bits, i.e either 11 bits plus sign or 12 bits without sign.
- Analog signals are conducted in shielded cables (see Fig. 2.1-5).
- The analog outputs can be used individually in a lot of different operating modes. The operating modes can be configured with system constants.
- Unused output channels may be left unconnected.

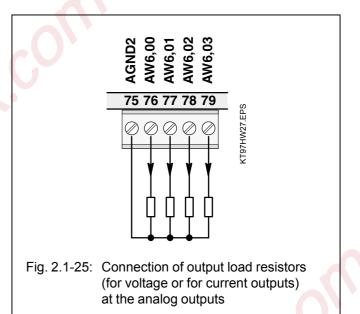
In the following, an application example for an analog receiver is shown.

Output ranges ±10 V / 0...20 mA / 4...20 mA

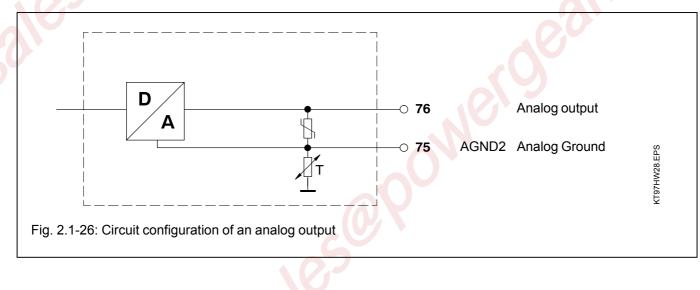
In case of voltage outputs the max, output current is ±3 mA. The output is short-circuit proof.

In case of current outputs, the range of permissible output load resistors is $0...500 \Omega$. If in case of an error the outputs are switched off, this means the following:

Configuration ±10 V 0 V Configuration 0...20 mA 0 mA Configuration 4...20 mA 0 mA.



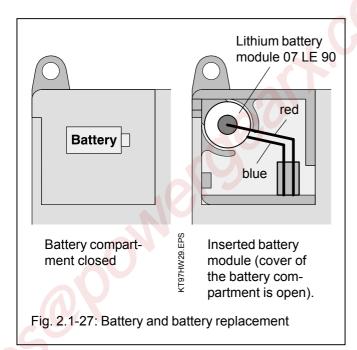
Circuit configuration of an analog output



2.1.4.9 Battery and battery replacement

- The lithium battery 07 LE 90 can be inserted into the battery compartment in order to
 - backup data of user program in RAM
 - backup data of additionally in RAM contained information, e.g. flag statuses
 - backup of time and date

The battery lifetime is typ. 5 years at 25°C. The battery lifetime is the time during which the device remains operable in order to backup data while the supply voltage of the basic unit is switched off. As long as there is a supply voltage available, there is no more load on the battery other than its own leakage current.



The following handling notes have to be observed:

- Use only lithium batteries approved by ABB.
- Replace the battery by a new one at the end of its life.
- Never short-circuit the battery!

There is danger of overheating and explosion. Avoid accidental short-circuits, therefore do not store batteries in metallic containers or boxes and do not bring them into contact with metallic surfaces.

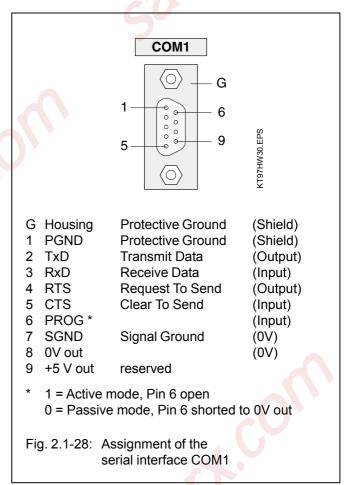
- Never try to charge a battery! Danger of overheating and explosion.
- Replace the battery only with the supply voltage switched on!
 Otherwise you risk data being lost.
- Dispose of battery environmentally consciously!
- If no battery is inserted or if the battery is exhausted, the red LED "Battery" lights up.

2.1.4.10 Serial interface COM1

Interface standard: EIA RS-232

Assignment of the serial interface COM1

The serial interface COM1 has the following pin assignment:

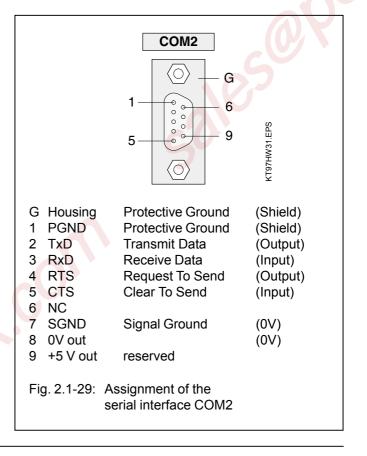


2.1.4.11 Serial interface COM2

Interface standard: EIA RS-232

Assignment of the serial interface COM2

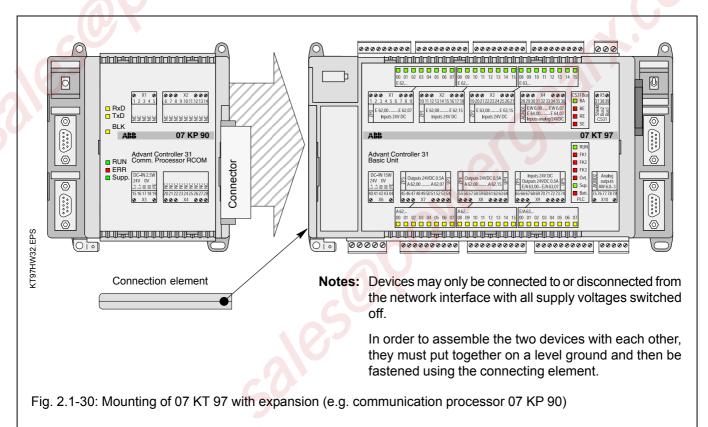
The serial interface COM2 has the following pin assignment:



2.1.4.12 Networking interface

The 07 KT 97 basic unit is equipped with a special parallel interface. It is thus possible to network it with another bus system using an additional communication processor module.

The additional communication processor has its own housing. Both housings (of the 07 KT 97 and of the communication processor) are assembled by means of a snap-on connection.



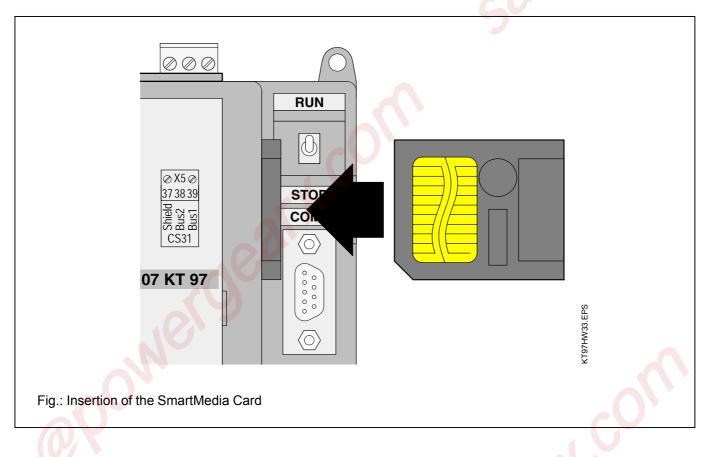
2.1-22

2.1.5 SmartMedia Card 07 MC 90

The SmartMedia Card serves for storing data up to 2 MB or 8 MB not being lost over an power OFF/ON cycle. It is used in the 07 KT 95...98 and 07 SL 97 basic units. It is recommended only to use ABB-proven SmartMedia Cards.

Field of application

- Storing and loading of PLC programs
- Storing and loading of user data
- Loading of firmware updates



Handling instructions

- The SmartMedia Card is inserted with the contact field visible (see the figure obove).
- A SmartMedia Card, once initialized as user data memory, can no more be used as a user program card.
- The SmartMedia Card must be protected from
 mechanical stress (e.g. do not bend)
 - electrostatic discharge
 - contact pollution (do not touch the contacts)

Important note

SmartMedia Cards with a supply voltage of 3.3 V cannot be used with basic units of the versions R01xx. They also cannot be used with 07 SL 97 basic units (see Usability).

Access

• Access within the PLC program is possible with function blocks, see documentation of the programming software.

Usability

SmartMedia Card 07 MC 90 **5 V** GJR5 2526 00 **R0101** (supply voltage 5 V, usable with the basic units 07 SL 97, 07 KT 95 to 07 KT 98 **R 01xx** and **R02xx**, all firmware versions, memory capacity 2 MB)

SmartMedia Card 07 MC 90 **3,3 V** GJR5 2526 00 **R0201** (supply voltage 3.3 V, usable with the basic units 07 KT 95 to 07 KT 98 **R02xx** with firmware versions as of V5.0, memory capacity 8 MB)

Technical data

Weight		2 g		
Dimensions		45 x 37 x	x 0.7 mm	
Order numbers 07 MC 90 07 MC 90	5 V 3.3 V	2 MB 8 MB	GJR5 2526 GJR5 2526	

2.1-23



2.1.6 High-speed counter

Features

The high-speed counter used in the basic unit 07 KT 97 works independently of the user program and is therefore able to response quickly to external signals. It can be used in seven different and configurable operating modes.

The desired operating mode is set in a system constant (see documentation part "System technology"). The configured operating mode is only activated during initialization (power-on, cold start, warm start). For all operating modes, the same function block **COUNTW** is used (see programming software).

Independent of the selected operating mode, the following features are valid:

- The pulses at the counter input or the evaluated signals at tracks A and B in case of connection of incremental position sensors are counted.
- The maximum counting frequency is 50 kHz.
- The counter uses the terminals 2 (E 62,00) and 3 (E 62,01) as fast inputs and, in one operating mode, also the output terminal 46 (A 62,00). In order to make all binary inputs and outputs available for other purposes than counting, it is possible, to disable the 07 KT 97's counting function.
- The counter can count upwards in all operating modes, in some modes it also can count dounwards. The counting range is from –32768 to +32767 or from 8000_H to 7FFF_H.

2.1.7 Technical Data 07 KT 97

In general, the technical system data listed under "System data and system configuration" in chapter 1 of volume 2 of the Advant Controller 31 system description are valid. Additional data or data which are different from the system data are listed as follows.

2.1.7.1 General data

Number of digital inputs Number of digital transistor outputs Number of digital inputs/outputs Number of analog inputs Number of analog outputs

I/O expansion via CS31 system bus by up to

Number of serial interfaces

Number of parallel interfaces

Integrated memory

Resolution of the integrated real-time clock

Data of the integrated high-speed hardware counter Number of operating modes Counting range Counting frequency

Processing time, 65 % bits, 35 % words

Number of software timers delay time of the timers

Number of up/down counter software blocks

Number of bit flags	in the addressable flag area
Number of word flags	"
Number of double word fl	ags "
Number of step chains	"
Number of constants KW	"
Number of constants KD	"

Indication of operating statuses and errors

Wiring method Power supply, CS31 system bus

all other terminals

2.1.7.2 Power supply

Rated supply voltage Current consumption Protection against reversed polarity 8 8 4

24

16

992 digital inputs
992 digital outputs
224 analog input channels
224 analog output channels
max. 31 remote modules altogether

2 (for programming or connection to man-machine communication)

1 special interface for connection of a communication processor (for networking with other bus systems)

Flash EPROM 512 kB (480 kB program + configuration data)

RAM 2 MB

(480 kB program with on-line programming + 256 kB variables)

1 second

7

-32768...+32767 (16 bits signed integer) max. 50 kHz

typ. 0.3 ms/kB program

any (max. 80 simultaneously active) 1 ms...24.8 days

any

60 LEDs altogether

removable screw-type terminal blocks max. 1 x 2.5 mm² or max. 2 x 1.5 mm² (see also page 2.1-9)

max. 1 x 1.5 mm²

24 V DC max. 0.35 A yes



2.1.7.3 Lithium battery

Battery for backup of RAM contents

Lifetime at 25°C

2.1.7.4 Digital inputs

Number of channels per module

Distribution of channels into groups

Common reference potential for group 1 (8 channels) for group 2 (8 channels) for group 3 (8 channels)

Electrical isolation

Signal coupling of input signals

Configuration possibilities of the inputs Input signal delay Channels E 62,00 and 62,01

Signalling of input statuses

Input signal voltage Signal 0 Signal 1

Input current per channel Input voltage = +24 V Input voltage = + 5 V Input voltage = +13 V Input voltage = +30 V

Max. cable length, unshielded Max. cable length, shielded

2.1.7.5 Digital outputs

Number of channels per module

Distribution of channels into groups

Common supply voltage for group 1 for group 2

Electrical isolation

Signalling of output statuses

Output current Rated value Maximum value Leakage current with signal 0

Demagnitization of inductive loads

Switching frequency with inductive loads

Switching frequency with lamp loads

Battery module 07 LE 90

typ. 5 years

24

3 groups of 8 channels each

ZP0 (channels 62,00...62,07) ZP1 (channels 62,08...62,15) ZP2 (channels 63,08...63,15)

between the groups, between groups and other circuitry (see also Fig. 2.1–4)

with optocoupler

typ. 7 ms (configurable to 1 ms) configurable for the high-speed counter

one green LED per channel, the LEDs correspond functionally to the input signals

-30 V...+ 5 V +13 V...+ 30 V

typ. 7.0 mA > 0.2 mA > 2.0 mA < 9.0 mA

600 m 1000 m

16 transistor outputs

2 groups of 8 channels each

UP3 (channels 62,00...62,07) UP4 (channels 62,08...62,15)

between the groups, between groups and other circuitry (see also Fig. 2.1–4)

one yellow LED per channel, the LEDs correspond functionally to the output signals

500 mA with UP3/4 = 24 V 625 mA with UP3/4 = 24 V + 25% < 0.5 mA

internally with a varistor

max. 0.5 Hz

max. 11 Hz with max. 5 W



- Max. cable length
- Short-circuit proof / overload proof

Protection of the outputs against reversed polarity

Forcing of 24 V DC at the outputs possible

Total load (via UP3 or UP4)

2.1.7.6 Digital inputs/outputs

Number of channels per module

Distribution of channels into groups

Common reference potential Common voltage supply

Electrical isolation

Signal coupling of the input signals

Configuration possibilities of the inputs Input signal delay, channels E 63,00...E 63,07

Signalling of input/output statuses

Input signal voltage (if used as inputs)

Signal 0 Signal 1

Input current per channel

Output current / switching frequency / inductive loads

Max. cable length

2.1.7.7 Analog inputs

Number of channels per module

Distribution of channels into groups

Common reference potential for group 1 (8 channels)

Electrical isolation

Max. permissible potential difference between Terminal M (minus pole of the power supply voltage) and terminal AGND (analog I/O minus pole)

Signalling of input statuses

Configuration possibilities (each channel), see 2.1.4.7

Input impedance per channel,

voltage input current input digital input 400 m (pay attention to voltage drops)

yes

yes

yes max. 4 A

8 inputs/outputs

1 group with 8 channels

ZP5 (channels E/A 63,00...E/A 63,07) UP5 (channels E/A 63,00...E/A 63,07)

between the group and other circuitry (see Fig. 2.1-4)

with optocoupler

typ. 7 ms (configurable to 1 ms)

one yellow LED per channel, the LEDs correspond functionally to the I/O signals

for details see Fig. 2.1-13 as well as the chapter "Circuit configuration of the digital inputs/outputs" -6 V...+ 5 V +13 V...+ 30 V

see Digital inputs

see Digital outputs

see Digital inputs/outputs

8

1 group with 8 channels

AGND1 (channels 06,00...06,07)

none (see also Fig. 2.1-4).

±1V

none

0...10 V, 0...5 V, ±10 V, ±5 V (also with differential signal) 0...20 mA, 4...20 mA Pt100 -50...+400°C and -50...+70°C (2-wire and 3-wire configuration) digital input

> 100 kΩ ca. 330 Ω ca. 4 kΩ



The current input has a self-protecting mechanism. If the input current gets too high, the shunt is switched off and the value for range overflow is generated. About every second, the unit tries to switch on the shunt again. In this way the correct measurement will succeed after the current has reached a normal value again.

Time constant of the input filter

Conversion cycle of current and voltage channels

Conversion cycle (by filtering time) of Pt100 channels

470 μ s with voltage, 100 μ s with current

Each configured input channel (U, I, Pt100) increases the conversion cycle of the U/I channels by typ. 1 ms.

Each configured input channel (U, I, Pt100) increases the conversion cycle of the Pt100 channels by typ. 50 ms.

Conversion cycle of unused input channels

Examples for the conversion cycle

Input channels configured as "unused" are skipped, i.e. they do not need any conversion time.

Example No.	1	2	3	4	5	6
Channels configured for U/I	1	8 *	-	-	2	4
Channels configured for Pt100	-		4	8	2	4
Channels configured as "unused"	7	-	4	-	4	-
Conversion cycle of U/I channels	1 ms	8 ms	-	-	4 ms	8 ms
Conversion cycle of Pt100 channels	<u> </u>	-	200 ms	400 ms	200 ms	400 ms

* Factory setting

, ,			
Resolution in bits	ranges ±10 V, 010 V ranges ±5 V, 05 V ranges 020 mA, 420 mA range -50 °C+70 °C range -50 °C+400 °C	11 bits plus sign 10 bits plus sign 12 bits without sign 10 bits plus sign 11 bits plus sign	
Resolution in mV, µA	range ±10 V range 010 V range 020 mA range 420 mA	ca. 5 mV ca. 5 mV ca. 5 μA ca. 4 μA	
Relationship between	input signal and hex code	-100 %0+100 % =	= 8008 _H 0000 _H 7FF8 _H (-32760032760 decimal)
	ey caused by non-linearity, tivity, ageing, adjustment error on ution: U, I Pt100	typ. 0.5 %, max. 1 % typ. 1 °C, max. 2 °C	
Threshold, if analog input is co	onfigured as digital input	ca. 7 V	
Max. cable length, 2-core shielded an	id cross section \geq 0,5 mm ²	100 m	
2.1.7.8 Analog out	puts		
Number of channels p	ber module	4	
Reference potential		AGND2 (channels 06	,0006,03)
Electrical isolation		none (see also Fig. 2	.1–4).
Terminal M (minus po	ential difference between le of the power supply voltage) analog I/O minus pole)	± 1 V	



Signalling of output statuses

Output signal ranges (configurable)

Output load capability of the voltage outputs

Resolution

Resolution (1 LSB), range -10 V...0...+10 V

Relationship between output signal and hex code

Conversion cycle for outputs

Conversion inaccuracy caused by non-linearity, temperature sensitivity, ageing, adjustment error on delivery and resolution

Max. cable length, 2-core shielded and cross section $\geq 0.5 \mbox{ mm}^2$

2.1.7.9 Connection of serial interfaces COM1 and COM2

Interface standard

Programming with 907 AC 1131

Program modifications with 907 AC 1131

Man-machine communication

Electrical isolation

Potential differences

Pin configuration and description of the COM1/COM2 interfaces

2.1.7.10 Connection to the CS31 system bus

Interface standard

Connection as a Master PLC as a Slave PLC

Setting of the CS31 module address

Electrical isolation

Terminal assignment and description of the CS31 bus interface

none

-10 V...0...+10 V 0...20 mA 4...20 mA

max. ±3 mA

12 bits

5 mV

-100 %...0...+100 % = 8008_{H} ...0000_H...7FF8_H (-32760...0...32760 decimal)

typ. 1 ms for each configured output channel

typ. 0.5 %, max. 1 %

100 m

EIA RS-232

with IBM PC (or compatible)

with IBM PC (or compatible)

yes, e.g. with an operating station

versus digital inputs and outputs, versus CS31 system bus interface (see also Fig. 2.1–4)

In order to avoid potential differences between the 07 KT 97 basic unit and the peripheral devices connected to the COM1/COM2 interfaces, these devices are supplied from the switch-gear cabinet socket (see also the earthing connections in Fig. 2.1-5).

see chapters 2.1.4.10 and 2.1.4.11

EIA RS-485

yes, transmitting and receiving areas are configurable yes, see "System constants"

yes, by system constant, stored in Flash EPROM of the Slave PLC

versus supply voltage, inputs and outputs, versus interfaces COM1/COM2 (see also Fig. 2.1-4)

see chapter 2.1.4.3

2.1.7.11 LED displays

LEDs for indication of:

- Statuses of digital inputs
- Statuses of digital outputs
- Statuses of digital inputs/outputs
- Power supply on
- Battery
- Program is running (RUN)
- Error classes (FK1, FK2, FK3)
- CS31 system bus is running (BA)
- bus-specific errors (BE, RE, SE)
- Overload/short-circuit of digital outputs

2.1.7.12 High-speed hardware counter

Data of the integrated high-speed hardware counter:

Configurable Counting range Counting frequency Used inputs Used outputs 1 green LED per channel 1 yellow LED per channel 1 yellow LED per channel 1 green LED 1 red LED 1 red LED per error class 1 green LED 3 red LEDs 1 red LEDs 1 red LED

in 7 operating modes -32768...+32767 (16 bits) max. 50 kHz E 62,00 and E 62,01 A 62,00

2.1.7.13 Mechanical data

Mounting on DIN rail

Fastening by screws

Width x height x depth

Wiring method Power supply terminals, CS31 system bus All other terminals

Weight

Dimensions for mounting

Advant Controller 31 / Issued: 01.2003

according to DIN EN 50022–35, 15 mm deep. The DIN rail is located in the middle between the upper and the lower edge of the module.

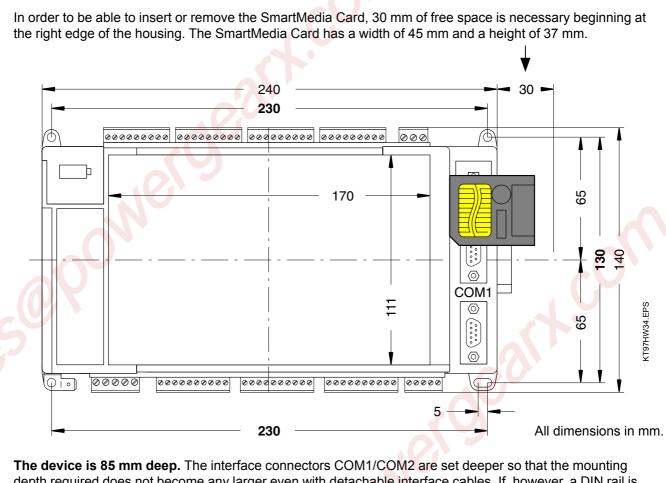
with 4 screws M4.

240 x 140 x 85 mm

by removable terminal blocks with screw-type terminals max. 1 x 2.5 mm² or max. 2 x 1.5 mm² max. 1 x 1.5 mm²

1.6 kg

see the following drawing



The device is 85 mm deep. The interface connectors COM1/COM2 are set deeper so that the mounting depth required does not become any larger even with detachable interface cables. If, however, a DIN rail is used, the mounting depth is increased by the overall depth of the rail.

The dimensions for assembly bore holes are printed in bold print.

Hardware

2.1.7.14 Mounting hints	
Mounting position	vertical, terminals above and below
Cooling	The natural convection cooling must not be hindered by cable ducts or other material mounted in the switch-gear cabinet.

2.1-31

07 KT 97 / Technical data

2.1.7.15 Ordering data

Basic unit 07 KT **97** R0100 Basic unit 07 KT **97** R0200 Basic unit 07 KT **97** R0120 (+ PROFIBUS-DP) Basic unit 07 KT **97** R0220 (+ PROFIBUS-DP) Basic unit 07 KT **97** R0160 (+ ARCNET) Basic unit 07 KT **97** R0260 (+ ARCNET) Basic unit 07 KT **97** R0162 (+ ARCNET + PROFIBUS-DP) Basic unit 07 KT **97** R0262 (+ ARCNET + PROFIBUS-DP) Basic unit 07 KT **97** R0270 (+ Ethernet) Basic unit 07 KT **97** R0272 (+ Ethernet + PROFIBUS-DP) Basic unit 07 KT **97** R0276 (+ Ethernet + ARCNET) Basic unit 07 KT **97** R0277 (+ Ethernet + Ethernet) Basic unit 07 KT **97** R0278 (+ Ethernet + CANopen) Basic unit 07 KT **97** R0280 (+ CANopen)

Scope of delivery

Basic unit 07 KT **96** R0100 Basic unit 07 KT **96** R0200 Scope of delivery

Basic unit 07 KT **95** R0100 Basic unit 07 KT **95** R0200 Scope of delivery

Accessories

System cable 07 SK 90		
System cable 07 SK 91		
System cable 07 SK 92		
Battery module 07 LE 90		
SmartMedia Card 07 MC 90	5.0 V	2 MB
SmartMedia Card 07 MC 90	3.3 V	8 MB

Further literature

System description ABB Procontic CS31 English System description Advant Controller 31 English Order No. GJR5 2530 00 R0100 Order No. GJR5 2530 00 R0200 Order No. GJR5 2530 00 R0220 Order No. GJR5 2530 00 R0220 Order No. GJR5 2530 00 R0260 Order No. GJR5 2530 00 R0260 Order No. GJR5 2530 00 R0262 Order No. GJR5 2530 00 R0272 Order No. GJR5 2530 00 R0272 Order No. GJR5 2530 00 R0276 Order No. GJR5 2530 00 R0277 Order No. GJR5 2530 00 R0277 Order No. GJR5 2530 00 R0278 Order No. GJR5 2530 00 R0278 Order No. GJR5 2530 00 R0278

Basic unit 07 KT 97 1 5-pole terminal block (5.08 mm) 3 10-pole terminal blocks (3.81 mm) 4 9-pole terminal blocks (3.81 mm) 1 5-pole terminal blocks (3.81 mm)

Order No. GJR5 2529 00 R0100 Order No. GJR5 2529 00 R0200

Basic unit 07 KT 96 1 5-pole terminal block (5.08 mm) 1 3-pole terminal block (5.08 mm) 2 10-pole terminal blocks (3.81 mm) 3 9-pole terminal blocks (3.81 mm)

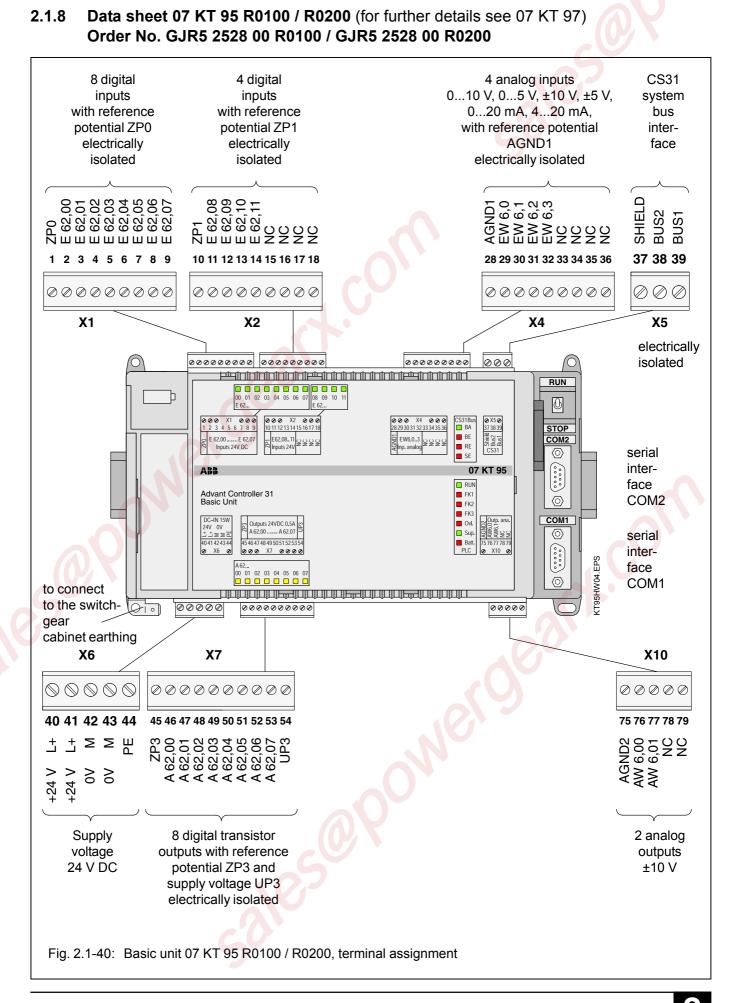
Order No. GJR5 2528 00 R0100 Order No. GJR5 2528 00 R0200

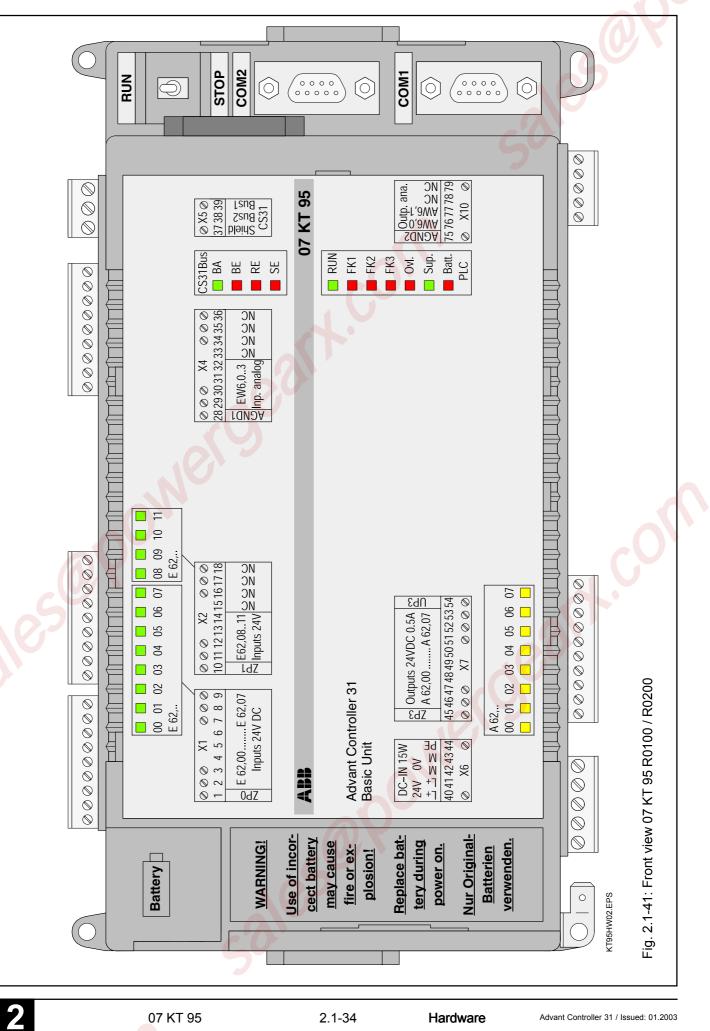
Basic unit 07 KT 95 1 5-pole terminal block (5.08 mm) 1 3-pole terminal block (5.08 mm) 1 10-pole terminal block (3.81 mm) 3 9-pole terminal blocks (3.81 mm) 1 5-pole terminal block (3.81 mm)

Order No. GJR5 2502 00 R0001 Order No. GJR5 2503 00 R0001 Order No. GJR5 2504 00 R0001 Order No. GJR5 2507 00 R0001 Order No. GJR5 2526 00 R0101 Order No. GJR5 2526 00 R0201

Order No. FPTN 4400 04 R2001 Order No. 1SAC 1316 99 R0201

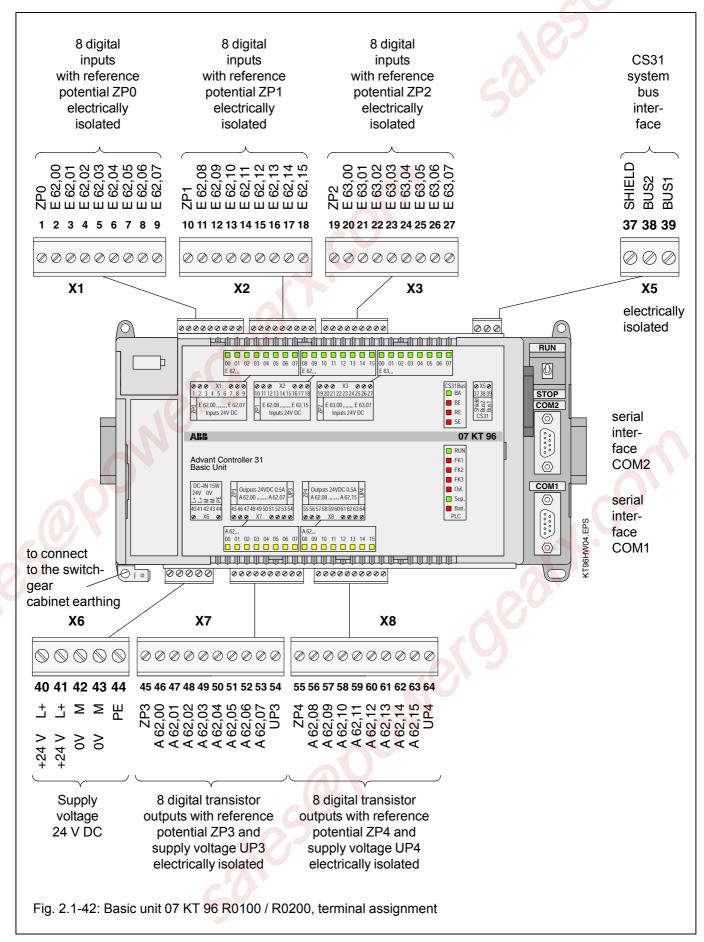


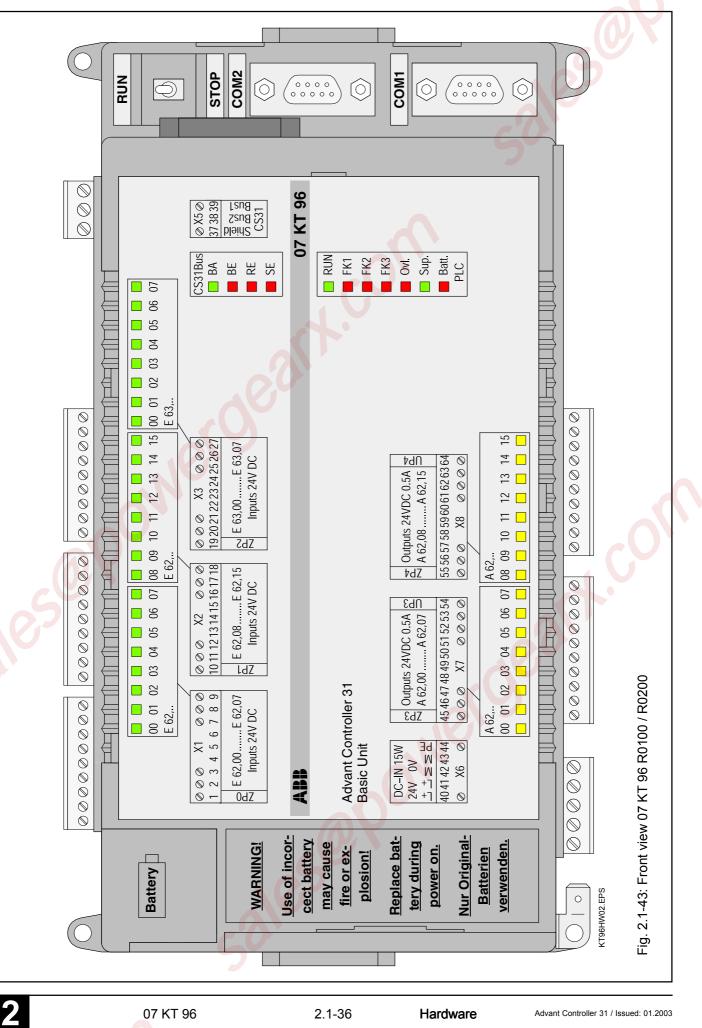




07 KT 95

2.1.9 Data sheet 07 KT 96 R0100 / R0200 (for further details see 07 KT 97) Order No. GJR5 2529 00 R0100 / GJR5 2529 00 R0200





07 KT 96

2.1.11 Description of ARCNET

2.1.11.1 Basic units with integrated ARCNET coupler

07 KT 97 R0160 (ARCNET) 07 KT 97 R0162 (ARCNET + PROFIBUS-DP) 07 KT 97 R0260 (ARCNET) 07 KT 97 R0262 (ARCNET + PROFIBUS-DP) 07 KT 97 R0276 (Ethernet + ARCNET)

2.1.11.2 Technical data

Connector X4

ARCNET interface

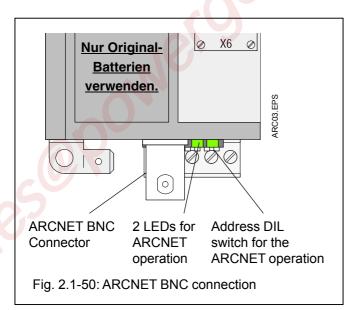
Recommended system cable

Cable length

Signalling green LED (BS)

green LED (TX)

Electrical isolation



2.1.11.3 ARCNET short description

 The ARCNET coupler is integrated in the housing of the basic unit. The DIL switch for setting the ARCNET address is accessible from the outside of the housing. The ARCNET coupler is powered by the internal 24 V DC supply voltage.

Note: The ARCNET interface is located on the upper side of the basic unit if there is also an Ethernet interface integrated.

 For ARCNET coupling, several function blocks are available. Order No. GJR5 2530 00 R0160 Order No. GJR5 2530 00 R0162 Order No. GJR5 2530 00 R0260 Order No. GJR5 2530 00 R0262 Order No. GJR5 2530 00 R0276

BNC

for coaxial cable

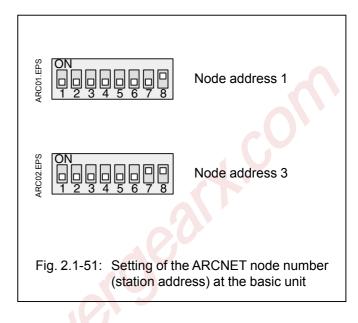
coaxial cable Type RG-62/U (char. impedance 93 Ω)

305 m in case of ARCNET bus with 8 stations. For further details see SMC TECHNICAL NOTE TN7-1.

operating condition "controller active", i.e. the PLC performs writing or reading operations

operating condition "transmit active", i.e. the PLC is sending on the ARCNET

versus power supply voltage, inputs and outputs, versus the interfaces COM1/COM2



- The ARCNET coupler interface is designed as a bus with BNC connector for coaxial cable. The ARCNET bus is earthed inside the module via a capacitor. As an EMC measure and for protection against dangerous contact voltages, the bus has to be earthed directly at a central place.
- Using the simplest configuration, called Linear ARC-NET, a coaxial cable (RG-62, 93 Ω) is laid from station to station and connected with T plugs at all stations. At both ends of the cable, terminating resistors with 93 Ω each have to be installed.



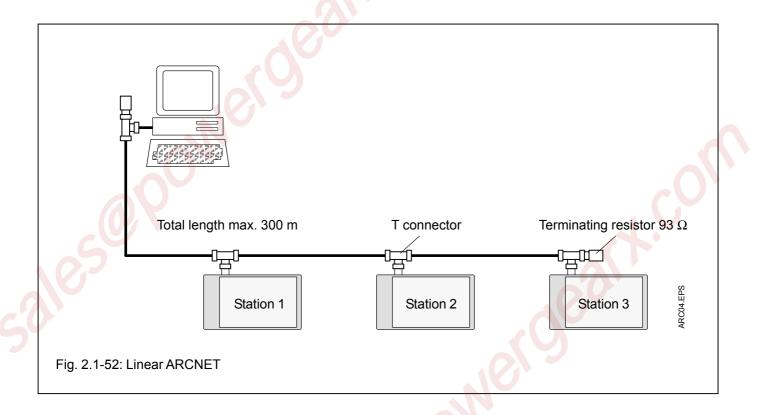
2.1.11.4 The ARCNET system (Attached Resource Computer Network)

- ARCNET is a system for data transmission in local networks.
- The ARCNET protocol is based on the Token Passing principle.
- By passing an identifier (token) from station to station it is guaranteed, that only one station can start a data transmission (transmission without collisions).
- The order of sequence, in which the stations are accessed, is automatically adapted by the existing conditions in the network, i.e. that the network is reconfigured automatically each time a station is added to the network or switched off.

2.1.11.4.1 The networking configurations

Linear ARCNET

- In the Linear ARCNET configuration, the stations are connected to one another directly, i.e. without using any distribution units.
- Each station is connected to the network by using a T connector.
- Both cable ends must be terminated by termination resistors.
- A maximum of 8 stations can be connected to one Linear ARCNET.
- The maximum cable length of the network is 300 m.
- An additional segment can be connected at the end of the wired segment via an Active Hub (active distribution unit), see next page.

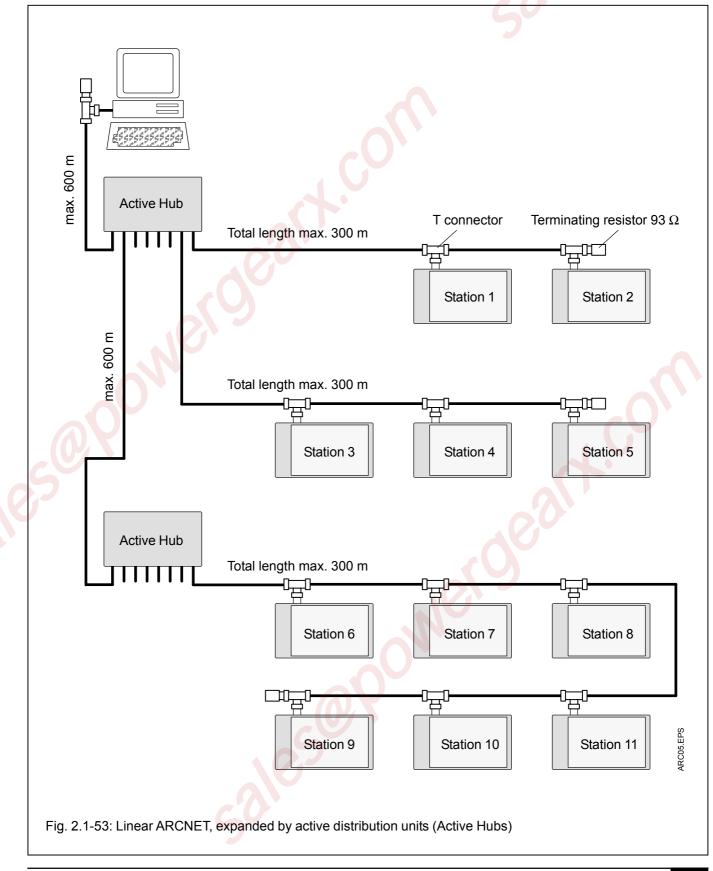




Linear ARCNET, expanded by active distribution units (Active Hubs)

• Active Hubs amplify the arriving signals. So they stabilize the network configuration and allow especially for high distances. The Active Hub decouples the station connectors from one another. Therefore, the entire network does not fail when one of the connections fails.

- The maximum length of the network is 6 km.
- A maximum of 255 stations can be used.



2.1.11.4.2 The features of the ARCNET system

- Data transmission rate 2.5 MBit/s
- Coaxial cable of type RG62/U, 93 Ω
- Coaxial plugs, suitable for the coaxial cable
- Maximum number of stations: 255

Maximum distances

- The maximum distance between two stations amounts to 6 km.
- The maximum distance between an Active Hub and an ARCNET station or between two Active Hubs amounts to 600 m.
- The maximum distance between a Passive Hub and an ARCNET station or between an Active Hub and a Passive Hub is 30 m. A Passive Hub works like a resistor network which carries out the cable termination at the stations.
- The maximum distance within a Linear ARCNET configuration is 300 m. A maximum of 8 stations can be connected.

2.1.12 Description of the PROFIBUS-DP coupler

2.1.12.1 Basic units with integrated PROFIBUS-DP coupler

07 KT 97 R120 (PROFIBUS-DP) 07 KT 97 R162 (ARCNET + PROFIBUS-DP) 07 KT 97 R220 (PROFIBUS-DP) 07 KT 97 R262 (ARCNET + PROFIBUS-DP) 07 KT 97 R272 (Ethernet + PROFIBUS-DP)

2.1.12.2 Technical data of the integrated coupler

Coupler type

Processor

Memory available

Internal supply with

Dimensions

2.1.12.3 Technical data of the interface

Interface connector

Transmission standard

Transmission protocol

Recommended system cable Characteristic impedance Cable capacitance Diameter of the wire cores (copper) Cross section of the cable cores Wire resistance per core Loop resistance (resistance of 2 cores)

Transmission speed (baud rate)

Maximum cable length

Spur lines

Electrical isolation of the interface

Display of statuses

Number of partitipants (masters/slaves) per bus segment

Number of partitipants via repeater

Order No. GJR5 2530 00 R0120 Order No. GJR5 2530 00 R0162 Order No. GJR5 2530 00 R0220 Order No. GJR5 2530 00 R0262 Order No. GJR5 2530 00 R0272

PROFIBUS coupler in PC/104 format

8-Bit processor with interrupt and DMA controller

8 kByte DP RAM, 512 kByte Flash EPROM, 368 kByte RAM

+5 V, 600 mA

96 x 91 x 13 mm

9-pole SUB-D, female

EIA RS-485

PROFIBUS-DP

shielded and twisted 2-core wire 135...165 Ω < 30 pF/m > 0.64 mm > 0.32 mm² < 55 Ω/km < 110 Ω/km

9.6 kBit/s bis 12000 kBit/s

1200 m with baud rate 9.6 / 19.2 / 93.75 kBit/s 1000 m with baud rate 187.5 kBit/s 400 m with baud rate 500 kBit/s 200 m with baud rate 1500 kBit/s 100 m with baud rate 3000 / 6000 / 12000 kBit/s

are only permitted up to max. 1500 kBit/s, they should be prevented with 500 kBit/s or more for security purposes

test voltage max. 850 V

with 4 LEDs (see Fig. 2.1-56)

max. 32

max. 126



2.1.12.4 PROFIBUS-DP coupler

Definitions, terms, abbreviations

PROFIBUS-DP	PROCESS FIELDBUS - DECENTRAL PERIPHERY
DP master (class 1) DP master (class 2) DP slave (DPS)	normal bus master commissioning device I/O module
DPV1	guideline for extended functions for PROFIBUS-DP
PNO	P ROFIBUS N utzer- O rgani- sation (user organization)

Standardization

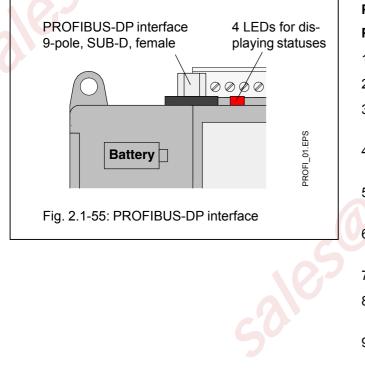
EN 50170, DIN 19245 Part 1, DIN 19245 Part 3, DPV1

Basics

PROFIBUS-DP is intended for fast data exchange in the field area. Here, central control units (e.g. PLC/PC) communicate with decentralized field devices like I/O, drives and valves via a fast serial connection. The data exchange with the decentralized modules is mainly performed cyclically. The communication functions, required for data exchange, are defined by the PROFIBUS-DP basic functions in accordance to EN 50170. For parametrization, diagnosis and alarm handling during the running cyclic data exchange, also non-cyclic communication functions are necessary for intelligent field devices.

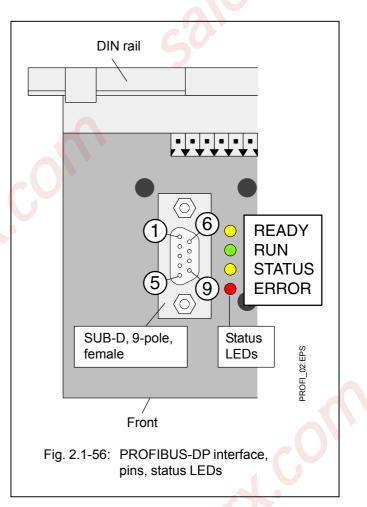
Location

The PROFIBUS-DP coupler is integrated in the housing of the basic unit. The bus interface is located on the top side to the left of the basic unit. There are also 4 LEDs for displaying statuses.



Pin assignment, meaning of the LEDs

The following figure shows the pin assignment of the PROFIBUS-DP interface as well as the names of the 4 LEDs. The drawing is shown looking from the front side (as mounted in the switch-gear cabinet).



Pin assignment (SUB-D, 9-pole, female)		
Pin No.	Signal	Meaning
1	Shield	shielding, protection earth
2	unused	
3	RxD/TxD-P	receive/transmit line, positive
4	CNTR-P	control signal for repeater, positive
5	DGND	reference potential for data exchange and +5 V
6	VP	+5 V (power supply for the bus terminating resistors)
7	unused	
8	RxD/TxD-N	receive/transmit line, negative
9	CNTR-N	control signal for repeater, negative
40	Llanduvan	Advent Centreller 24 / Jeound 0

Pin assignment (SUB-D, 9-pole, female)

Bus termination

The line ends (of the bus segments) must be equipped with bus termination resistors (show the drawing to the right). Normally, the resistors are integrated in the interface connectors.

VP (+5 V) (6) ————	
Data line $\mathbb{P}(\mathbb{P} \times \mathbb{P}(\mathbb{T} \times \mathbb{P}, \mathbb{P}))$ (2)	
Data line B (RxD/TxD-P) (3) 220 Ω	
Data line A (RxD/TxD-N) (8)	
390 Ω	EPS
DGND (0 V) (5) ―――ゴ	PROFI_03.EPS
Fig. 2.1-57: PROFIBUS-DP interface, bus termination on the line ends	PRO

Status LEDs

LED	Color	Condition	Meaning
READY	yellow	on flashes cyclic flashes non-cyclic off	coupler ready bootstrap loader active hardware or system error defective hardware
RUN	green	on flashes cyclic flashes non-cyclic off	communication is running communication is stalled missing or erroneous configuration no communication
STATUS	yellow	on off	DP slave: data exchange with DP master DP master: transmits data or token DP slave: no data exchange DP master: no token
ERROR	red	on off	PROFIBUS error no error

The condition of the PROFIBUS coupler is indicated with the 4 status LEDs.

After power ON the coupler initializes a self-test. If this test was successful, the yellow READY LED goes ON. Otherwise the LED starts flashing and aborts the further initialization. If the LED remains OFF, the coupler is defective.

In the course of initialization, the RUN LED is OFF for the first time. The LED is only activated after configuration data has been sent to the coupler and the operating mode of the coupler was set. If the operating system of the coupler detects a parameterization or a configuration error, the green RUN LED flashes non-cyclically. If this LED flashes cyclically, the coupler is ready for communication, but the communication is not active yet. In case of an active communication, the RUN LED lights continuously.

The red ERROR LED indicates errors on the PROFIBUS interface.

In the operating mode DP slave, the yellow STATUS LED indicates the active I/O data exchange with the DP master. In the operating mode DP master, the STATUS LED indicates the ownership of the token and therefore the I/O data exchange with the involved DP slaves.

During the initialization procedure and also if the coupler is configured (anew) - in particular if the operating mode was changed - it can occur that all or some LEDs light up for a short period of time, before reaching a defined condition.

Important address

PROFIBUS Nutzerorganisation e. V. (PNO) Haid-und-Neu-Straße 7 D-76131 Karlsruhe

Tel.:	(+49) 721	9658 590
Fax:	(+49) 721	9658 589

Internet: http://www.profibus.com

Advant Controller 31 / Issued: 01.2003

2.1.13 Description of the CANopen Master coupler

2.1.13.1 Basic units with an integrated CANopen Master coupler

07 KT 97 R278 (Ethernet + CANopen) 07 KT 97 R280 (CANopen)

2.1.13.2 Technical data of the integrated coupler

Coupler type

Processor

Memory available

Internal supply with

Dimensions

CE sign

2.1.13.3 Technical data of the interface

Interface connector

Transmission standard

Transmission protocol

Transmission speed (baud rate)

Display of statuses

Number of partitipants

Order No. GJR5 2530 00 R0278 Order No. GJR5 2530 00 R0280

CANopen Master coupler in PC/104 format

16-bit processor with interrupt and DMA controller

8 kbyte DP-RAM, 512 kbyte Flash EPROM, 128 kbyte RAM

+5 V, 650 mA

96 x 90 x 23 mm

55011 Class b for emission, EN 50082-2 for noise immunity

5-pole COMBICON, female ISO 11898, isolated CANopen (CAN), max. 1 MBaud 20 kbit/s, 125 kbit/s, 250 kbit/s, 500 kbit/s und 1 Mbit/s by 4 LEDs (see Fig. 2.2-59) max. 127 slaves

2.1.13.4 Short description

CANopen is a standardized 7-layer protocol for decentralized industrial automation systems, based on the Controller Area Network (CAN) and the CAN Application Layer (CAL).

CANopen bases on a communication profile in which the basic communication mechanisms and their descriptions are defined, e.g. mechanisms for interchange of process data in real time or transmitting of alarm messages.

The different CANopen device profiles make use of this common communication profile. The device profiles describe the specific functionality of a device class or its parameters. For the most important device classes used in the industrial automation technology, such as digital and analog input/output modules, sensors, drives, operator panels, loop controllers, programmable control systems and encoders, suitable device profiles exist. Others are in preparation.

A central element of the CANopen standard is the description of the device functionality in an object directory. The object directory is subdivided into a general part and a device-specific part. The general part contains details on the device, such as device identification, name of manufacturer, communication parameters etc. The devicespecific part describes the specific functionality of the concerned device. These features of a CANopen device are described in a standardized Electronic Data Sheet (EDS).

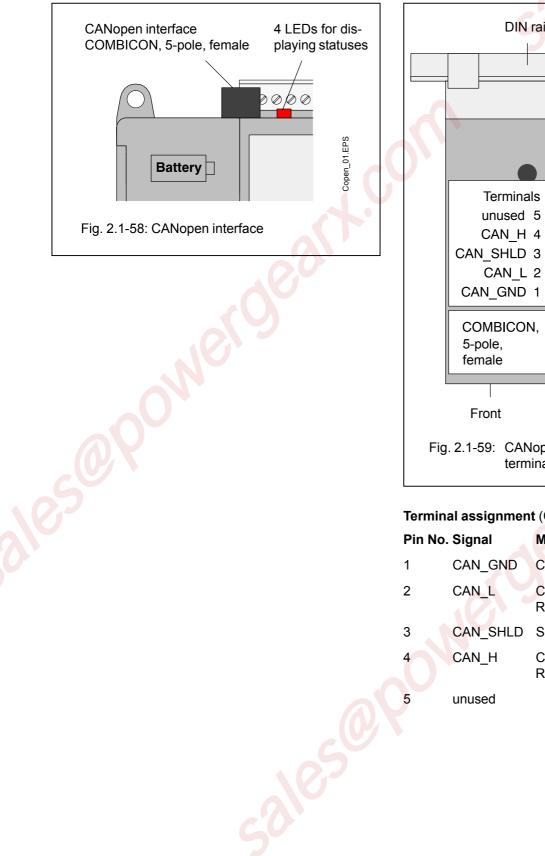
A CANopen network consists of a maximum of 128 devices, one NMT master and a maximum of 127 NMT slaves. In contrast to other typical master-slave systems such as PROFIBUS, the CANopen terms Master and Slave have a different meaning.

In operational mode, all devices are able to transmit messages via the bus. In addition, the master can change the operating mode of the slaves.

Normally a CANopen master is realized by a PLC or a PC. The bus address of a CANopen slave can be set from 1 to 127. By the device address, a number of identifiers are created, which are then used by the device.

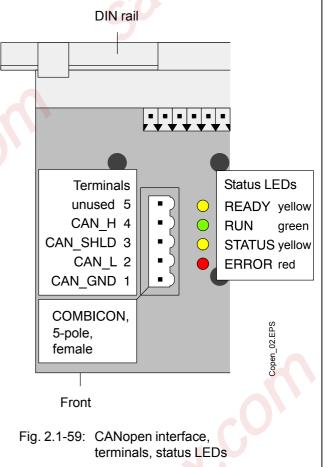
2.1.13.5 Location

The CANopen coupler is integrated in the housing of the basic unit. The bus interface is located on the top side to the left of the basic unit. There are also the 4 LEDs for displaying statuses.



2.1.13.6 Pin assignment, meaning of the LEDs

The following figure shows the pin assignment of the CANopen interface as well as the names of the 4 LEDs. The drawing is shown looking from the front side (as mounted in the switch-gear cabinet).



Terminal assignment (COMBICON, 5-pole, female)

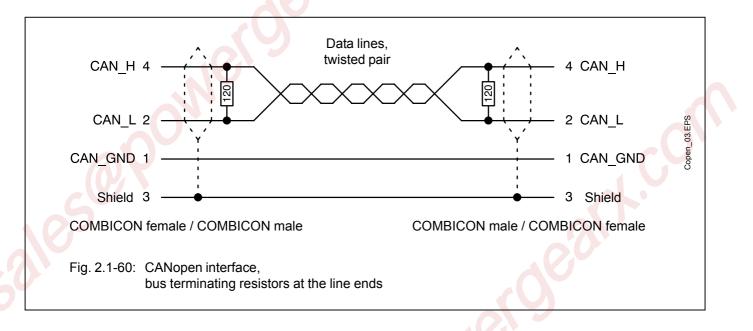
Pin No. Signal		Meaning	
1	CAN_GND	CAN Ground	
2	CAN_L	CAN_L bus line, Receive/Transmit low	
3	CAN_SHLD	Shield of the bus cable	
4	CAN_H	CAN_H bus line, Receive/Transmit high	
5	unused		

Status LEDs

LED	Color	Condition	Meaning
READY	yellow	on flashes cyclic flashes non-cyclic off	Coupler ready Bootstrap loader active Hardware or system error Defective hardware
RUN	green	on flashes cyclic flashes non-cyclic off	Communication is running Communication is stalled Missing or faulty configuration No communication
STATUS	yellow	on off	Coupler transmits data Coupler does not transmit data
ERROR	red	on off	CANopen error No error

2.1.13.7 Bus termination

The data line ends must be equipped with 120-Ohm bus terminating resistors. Normally, the resistors are integrated in the interface connectors.



2.1.14 Description of the Ethernet coupler

2.1.14.1 Basic units with integrated Ethernet coupler

07 KT 97 R270 (Ethernet) 07 KT 97 R272 (Ethernet + PROFIBUS-DP) 07 KT 97 R276 (Ethernet + ARCNET) 07 KT 97 R277 (Ethernet + Ethernet) 07 KT 97 R278 (Eternet + CANopen)

2.1.14.2 Technical data of the integrated coupler

Coupler type

Processor

Ethernet controller

Interfaces Ethernet Diagnosis

LED displays

Setting the station identifier

Internal supply with

Dimensions

2.1.14.3 Technical data of the software

Firmware

Protocol suite

EthernetIP

2.1.14.4 Short description

The Ethernet coupler is an intelligent 100-Base-T-Ethernet communication interface based on the highly integrated microcontroller EC1. The coupler supports the complete TCP/IP protocol and the application layers, too.

The user interface is based on a dual-port memory.

The coupler meets the PC/104 standard. It is powered by the internal 5 V supply voltage.

Order No. GJR5 2530 00 R0270 Order No. GJR5 2530 00 R0272 Order No. GJR5 2530 00 R0276 Order No. GJR5 2530 00 R0277 Order No. GJR5 2530 00 R0278

Ethernet coupler in PC/104 format

EC1-160, system clock 48 MHz

EC1-160, internally

10 / 100 BASE-TX / RJ45 MiniDIN, 8-pole, female

RDY - System o.k. RUN - Configuration o.k. / Communication is running ERR - Communication error STA - Status of the Ethernet communication

 $00_{\rm H}$ to $\rm FF_{\rm H}$

+5 V, 300 mA

90 x 96 x 23 mm

UDP/IP TCP/IP Open MODBUS on TCP others in preparation

Slave only Cyclic and non-cyclic data transfer

The Ethernet communication is run via an RJ45 interface. In addition, the coupler has a diagnosis interface in Mini-DIN format.

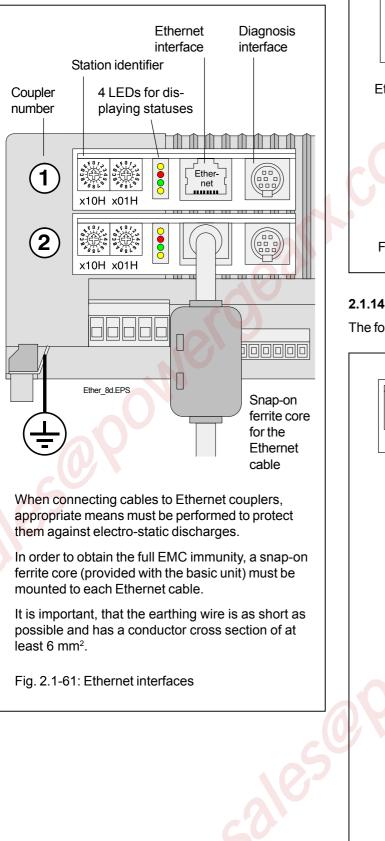
The coupler is configured via the dual-port memory, the diagnosis interface or a TCP/IP connection by means of a system configurator. The configuration is saved non-volatile in a Flash EPROM.

Advant Controller 31 / Issued: 05.2003



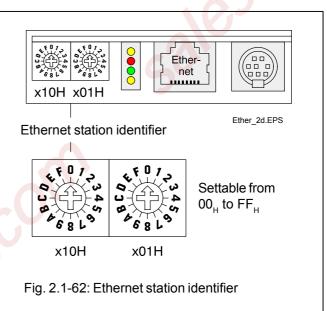
2.1.14.5 Location

The following figure shows a basic unit with two Ethernet couplers, which are located on the bottom side to the left of the basic unit. Units which have only one coupler, use coupler No. 1.



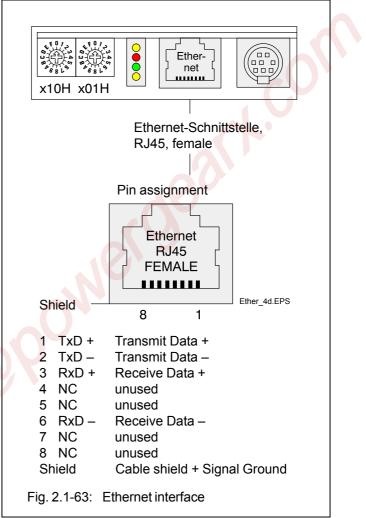
2.1.14.6 Station identifier

The following figure shows the setting of the station identifier.



2.1.14.7 Ethernet interface

The following figure shows the Ethernet interface.



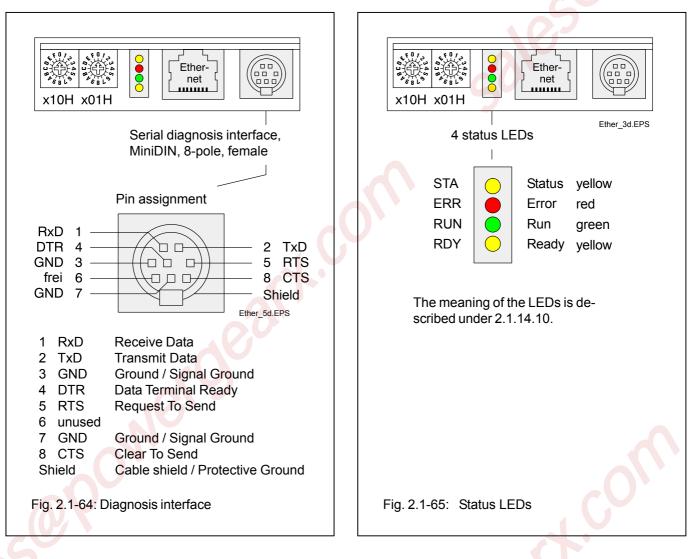


2.1.14.8 Diagnosis interface

The following figure shows the diagnosis interface.

2.1.14.9 Meaning of the LEDs

The following figure shows the 4 status LEDs.



2.1.14.10 Status LEDs

LED	Color	Condition	Meaning
STATUS	yellow	flashes	Ethernet frame detected on the network
ERROR	red	on off	Error No error
RUN	green	eon flashes cyclic flashes non-cyclic off	Communication is running Ready for communication Parameterization error No communication
READY	yellow	on flashes cyclic flashes non-cyclic off	Coupler is ready Bootstrap loader is active Hardware or system error Defektive hardware











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